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APEX SCIENCE & ENGINEERING CORP.

(OPTOELECTRONIC DIV.)

Http : www.apexgroup.com.tw

4F, No. 112, Shin - Min St., Chungho City, 235, Taiwan, R.O.C.

Tel : 886 - 2 - 2228 - 7331 Fax : 886 - 2 - 2221 - 9105

HC204001BNHDWB-V1

DATA SHEET

ISSUE	VERSION	APPROVER	CHECKER	ENGINEER
	A		Michael	KEVIN_LIN

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY MODULE
MODEL NO: HC204001BNHDWB-V1

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1. SCOPE

The HC204001BNHDWB-V1, dot-matrix LCD unit of a 5 x 7- dot 20-character 4-line dot-matrix LCD panel, LCD driver, controller LSI and White edge-light LED fabricated on a single PCB. Incorporating mask ROM-based character generator and display data RAM in the controller LSI, the unit can efficiently display the desired characters under microprocessor control.

2. PRODUCT SPECIFICATIONS

2.1 General

- The LCD of the unit is STN , Negative , Wide temperature type.
- Low power consumption with the dot-matrix LCD panel and CMOS LSI. Built-in edge-light white LED with high luminance and stable radiation.
- Thin, lightweight design permits easy installation in a variety of equipment.
- Allowing for being connected at general-purpose CMOS signal level, the unit can be easily interfaced to a microprocessor with common 4-bit and 8-bit parallel inputs and outputs.
- Multiplexing driving : 1/16duty, 1/4bias, 6 o'clock
- Built-in character generator ROM and RAM, and display data RAM:
 - Character generator ROM
 - 225 different 5 x 7 dot-matrix character patterns (Alphanumeric and symbols)
 - Character generator RAM
 - 8 different user programmed 5 x 7 dot-matrix patterns
 - Display data RAM
 - 80 x 8 bits
- Numerous instructions
 - Display clear, Cursor home, Display ON/OFF, Cursor ON/OFF, Blink character, Cursor shift, Display shift
- **ROHS**

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2.2 Mechanical Characteristics

Item	Characteristic
Number of Characters	20 × 4
Dot dimensions(mm)	0.55 × 0.55
Dot spacing (mm)	0.05
Module dimensions (Horizontal × Vertical × Thickness, mm)	98.0 × 60.0 × 12.0max.
Viewing area (Horizontal × Vertical, mm)	76.0 × 25.2
Active area (Horizontal × Vertical, mm)	70.4 × 20.8

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2.3 Absolute Maximum Ratings (Without LED back-light)

Characteristic	Symbol	Unit	Value
Power Supply Voltage (1)	V_{DD}	V	5V
Power Supply Voltage (2)	V_{LCD}	V	1V~0.8V
Power Supply Voltage	V_{IN}	V	5V

2.4 Electrical Characteristics (Without LED back-light)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Operating Voltage	V_{DD}	--	4.5	--	5.5	V
Supply Current	I_{DD}	Internal oscillation or external clock ($V_{DD}=5.0V, f_{OSC}=270kHz$)	--	0.35	0.6	mA
Input Voltage(1) (except OSC1)	V_{IH1}	--	2.2	--	V_{DD}	V
	V_{IL1}	--	-0.3	--	0.6	
Input Voltage(2) (OSC1)	V_{IH2}	--	$V_{DD}-0.1$	--	V_{DD}	V
	V_{IL2}	--	-0.2	--	1.0	
Output Voltage(1) (DB0 to DB7)	V_{OH1}	$I_{OH}=-0.205mA$	2.4	--	--	V
	V_{OL1}	$I_{OL}=1.2mA$	--	--	0.4	
Output Voltage(2) (DB0 to DB7)	V_{OH2}	$I_O=-40\mu A$	$0.9V_{DD}$	--	--	V
	V_{OL2}	$I_O=40\mu A$	--	--	$0.1V_{DD}$	
Voltage Drop	V_{dCOM}	$I_O=0.1mA$	--	--	1	V
	V_{dSEG}		--	--	1	
Input Leakage Current	I_{IKG}	$V_{IN}=0V$ to V_{DD}	-1	--	1	μA
Input Low Current	I_{IL}	$V_{IN}=0V, V_{DD}=5V$ (Pull Up)	-50	-125	-250	
Internal Clock (external Rf)	f_{OSC1}	$Rf=91k\Omega \pm 2\%$ ($V_{DD}=5V$)	190	270	350	kHz
External Clock	f_{OSC}	--	125	270	410	kHz
	duty		45	50	55	%
	t_R, t_F		--	--	0.2	μS
LCD Driving Voltage	V_{LCD}	$V_{DD}-V_5$ (1/5, 1/4 Bias)	3.0	--	13.0	V

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2.5 Optical Characteristics

Absolute maximum ratings

Item	Symbol	Rating	Unit	Remarks
Storage temperature range	Tst	-30~80	°C	No condensation
Operating temperature range	Top	-20~70	°C	No condensation

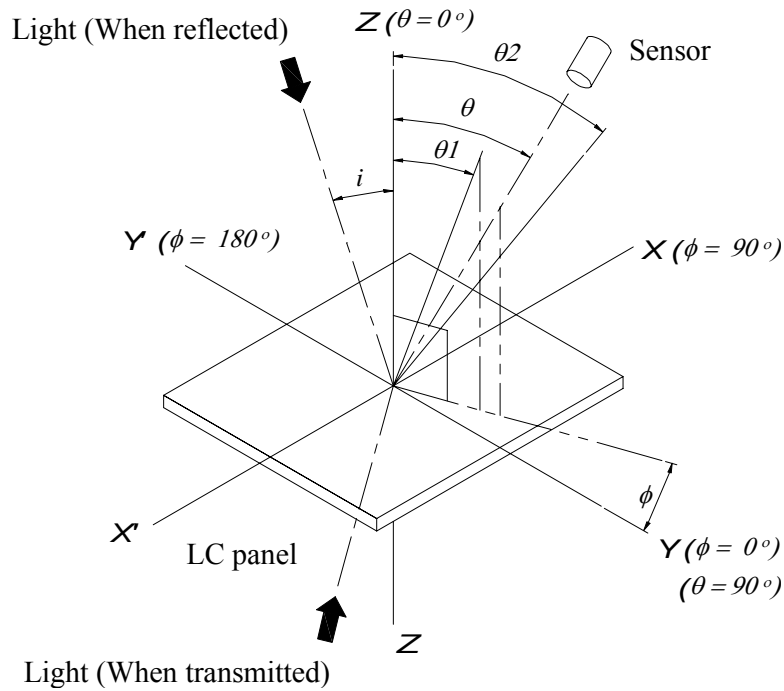
2.6 Optical Characteristics

1/16 duty, 1/4 bias

Item	Symbol	Temp.	Min.	Typ.	Max.	Unit
Driving voltage	Vop	0 °C	--	4.50	--	V
		25 °C	--	4.20	--	
		50 °C	--	3.8	--	
Contrast	Cr	25 °C	3	4	--	--
Frame freq.	f	--	32	64	150	Hz
Viewing angle*	θ_1	25 °C	-40	--	40	deg.
	θ_2		-40	--	-40	
Response time	t _{on}	25 °C	--	170	260	ms
	t _{off}		--	240	360	

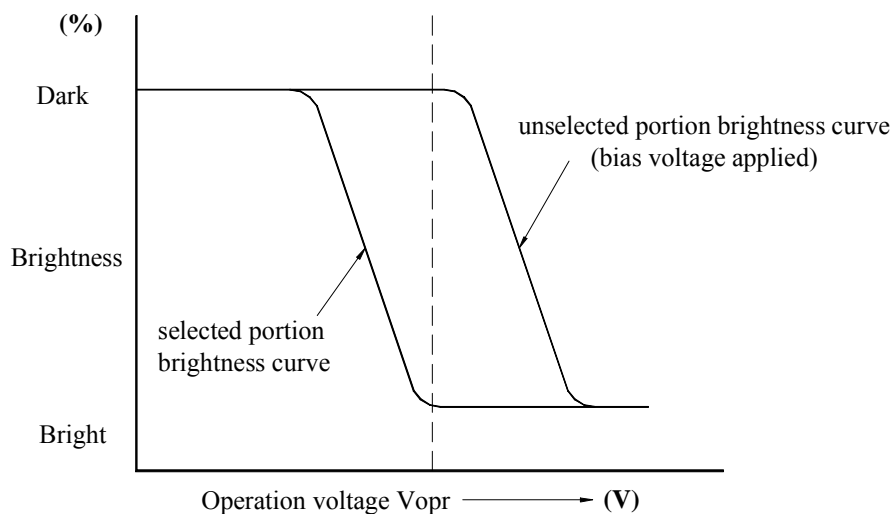
2.6.1 Definition of optical characteristics

* Definition of angles ϕ and θ



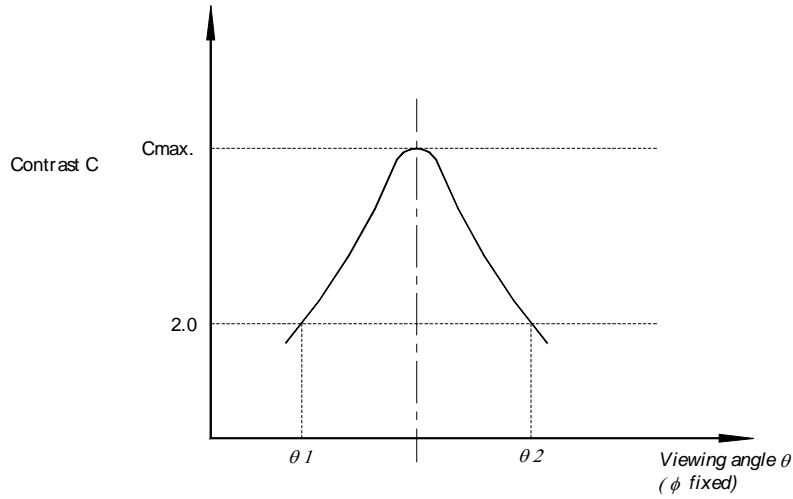
*Definition of contrast C

$$C = \frac{B1}{B2} = \frac{\text{Brightness of selected portion}}{\text{Brightness of unselected portion}}$$



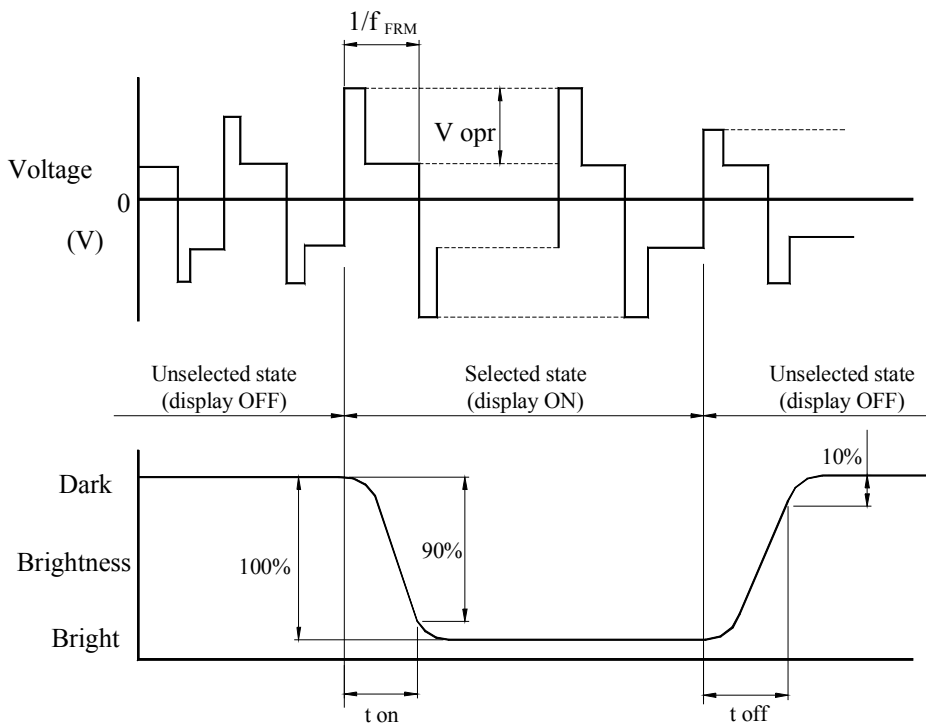
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* Definition of viewing angles θ_1 and θ_2



Note : Optimum vision with the naked eye and viewing angle θ at C_{max} above are not always the same.

* Definition of response time



V_{opr} : Operating voltage (V)

t_{on} : Response time (rise) (ms)

f_{FRM} : Frame frequency (Hz)

t_{off} : Response time (fall) (ms)

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2.7 LED Back-light Characteristics

2.7.1 Electrical / Optical specifications

Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward voltage	V _f	If=60mA, White	--	3.6	4.0	V
*Luminous Intensity	I _v	If=60mA, White	140	200	--	cd/m ²
Luminous Tolerance	--	If=60mA, White	--	--	20	%
Chromaticity Coordinate*	X	If=60mA, White	--	0.31	--	--
Chromaticity Coordinate*	Y	If=60mA, White	--	0.32	--	--

Note: * Please refer to CIE 1931 Chromaticity diagram.

2.7.2 Absolute maximum ratings

Item	Symbol	White	Unit
Forward Current	I _F	70	mA
Pulse Forward Current	I _{FP}	150	mA
Power Dissipation	P _{AD}	300	mW
Reverse Voltage	V _R	5	V
Operating Temp.	T _{opr}	-20~60	°C
Storage Temp.	T _{stg}	-30~70	°C

3. RELIABILITY

3.1 Reliability

Test item	Test condition	Evaluation and assessment
Operation at high temperature and humidity	40 °C±2 °C 90%RH for 500hours	No abnormalities in functions* and appearance**
Operation at high temperature	60 °C±2 °C for 500 hours	No abnormalities in functions* and appearance**
Heat shock	-20± ~ +60 °C Left for 1 hour at each temperature, transition time 5 min, repeated 10times	No abnormalities in functions* and appearance**
Low temperature	-20±2 °C for 500 hours	No abnormalities in functions* and appearance**
Vibration	Sweep for 1 min at 10 Hz, 55Hz, 10Hz, amplitude 1.5mm 2 hrs each in the X,Y and Z directions	No abnormalities in functions* and appearance**
Drop shock	Dropped onto a board from a height of 10cm	No abnormalities in functions* and appearance**

* Dissipation current, contrast and display functions

** Polarizing filter deterioration, other appearance defects

3.2 Liquid crystal panel service life

100,000 hours minimum at 25 °C±10 °C

3.3 Definition of panel service life

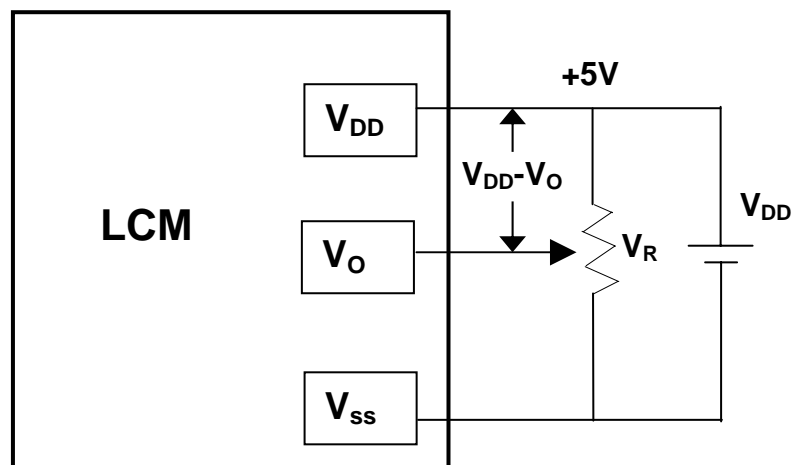
- Contrast becomes 30% of initial value
- Current consumption becomes three times higher than initial value
- Remarkable alignment deterioration occurs in LCD cell layer
- Unusual operation occurs in display functions

4. OPERATING INSTRUCTIONS

4.1 Input signal Function

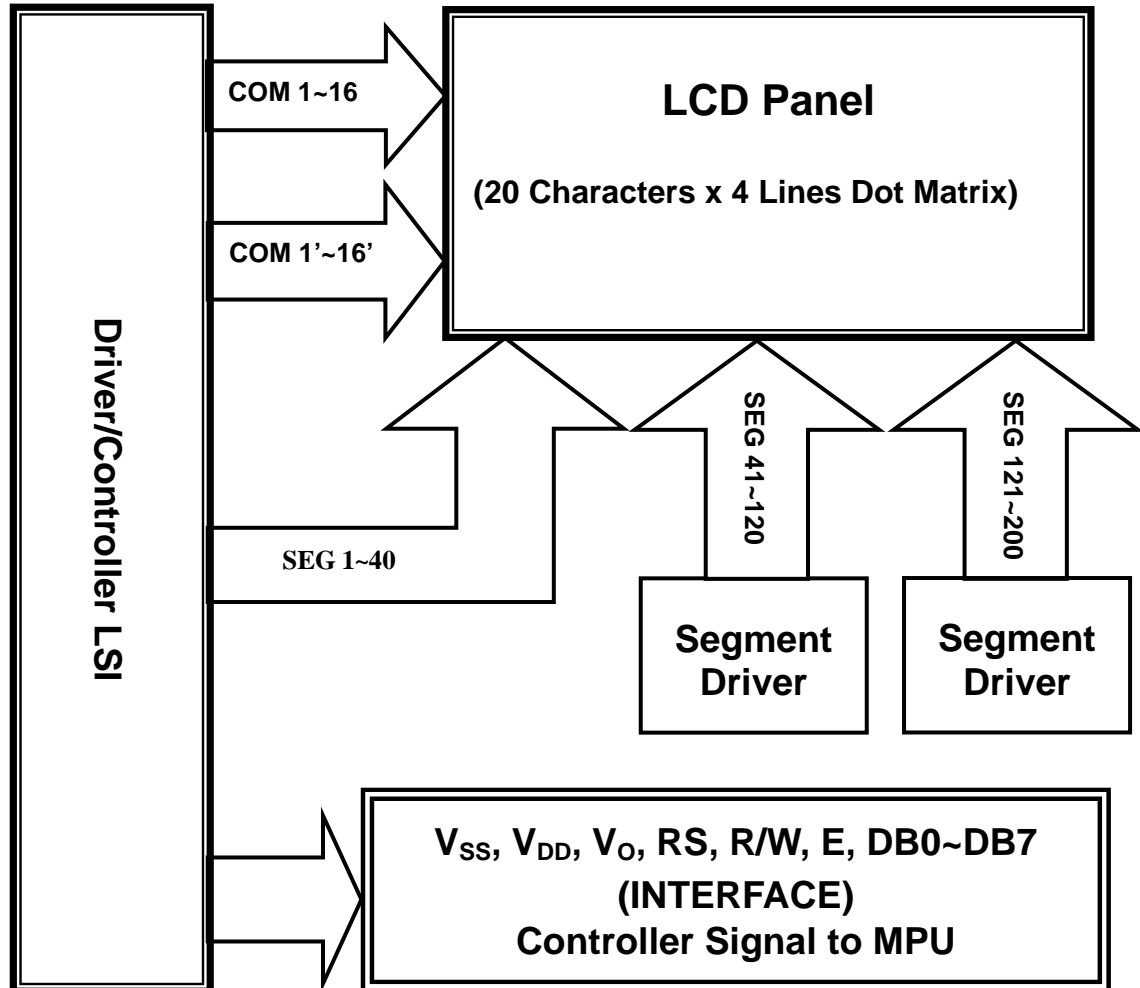
NO.	Symbol	Function
1	VSS	Ground (0V)
2	VDD	Power supply for Logic circuit
3	VO	Power Supply for Driving the LCD
4	RS	Data / Instruction select
5	R/W	Read / Write select
6	E	Enable signal
7-14	DB0-DB7	Data Bus line
15	LED A	Power supply for LED
16	LED K	Power supply for LED

4.2 Voltage Generator Circuit



$V_{DD} - V_{EE}$: LCD Driving Voltage
 V_R : 10K~20K

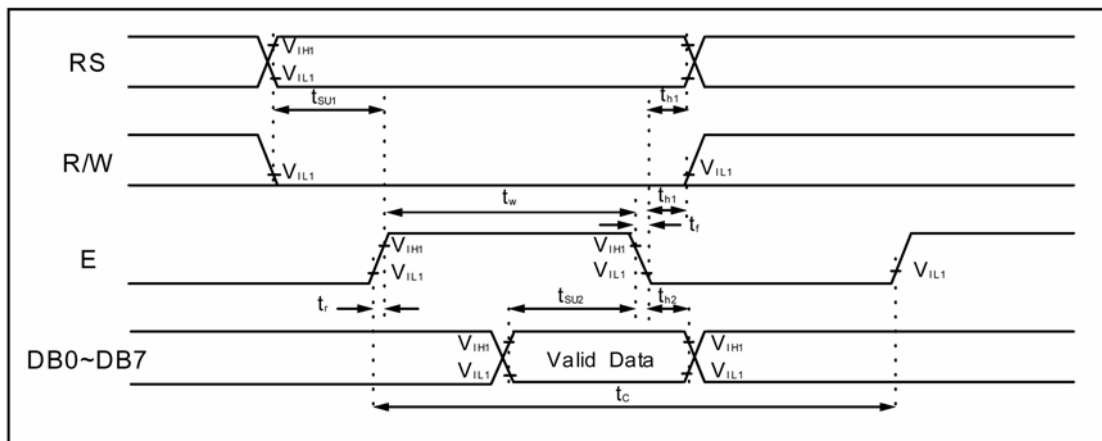
4.3 Circuit Block Diagram



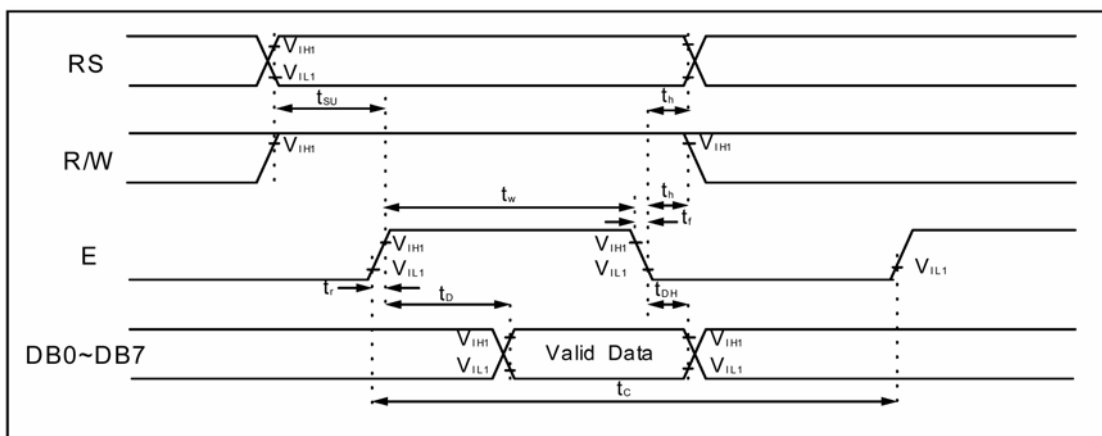
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4.4 Timing Characteristics

Mode	Characteristic	Symbol	Min.	Typ.	Max.	Unit
Write Mode	E Cycle Time	t_c	500	--	--	ns
	E Rise/fall Time	$t_{R,tF}$	--	--	20	
	E Pulse Width (High,Low)	t_w	230	--	--	
	R/W and RS Setup Time	t_{su1}	40	--	--	
	R/W and RS Hold Time	t_{H2}	10	--	--	
	Data Setup Time	t_{su2}	80	--	--	
	Data Hold Time	t_{H2}	10	--	--	
Read Mode	E Cycle Time	t_c	500	--	--	ns
	E Rise/fall Time	$t_{R,tF}$	--	--	20	
	E Pulse Width (High,Low)	t_w	230	--	--	
	R/W and RS Setup Time	t_{su}	40	--	--	
	R/W and RS Hold Time	t_H	10	--	--	
	Data Output Delay Time	t_D	--	--	120	
	Data Hold Time	t_{DH}	5	--	--	



Write Mode Timing Diagram



Read Mode Timing Diagram

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4.5 Instruction table

Instruction	Instruction Code										Description	Execution time ($f_{osc}=270kHz$)	
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0			
Clear Display	0	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H" from AC	1.53ms
Return Home	0	0	0	0	0	0	0	0	0	1	-	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53ms
Entry Mode Set	0	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and enable the shift of entire display.	39 μ s
Display ON/OFF Control	0	0	0	0	0	0	0	1	D	C	B	Set display(D), cursor(C), and blinking of cursor (B) on/off control bit.	39 μ s
Cursor or Display Shift	0	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	39 μ s
Function Set	0	0	0	0	1	DL	N	F	-	-	-	Set interface data length (DL:8-bit/4-bit), numbers of display line (N:2-line/1-line) and, display font type (F:5x11 dots/5x8 dots)	39 μ s
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	AC0	Set CGRAM address in address counter.	39 μ s
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	AC0	Set DDRAM address in address counter.	39 μ s
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 μ s
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	D0	Write data into internal RAM (DDRAM/CGRAM)	43 μ s
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	D0	Read data from internal RAM (DDRAM/CGRAM)	43 μ s

"--": don't care

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4.6 Font table

Upper 4 bit Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL	CG RAM (1)			0	1	2	3	4				5	6	7	8	9
LLLH	(2)		:	;	<	=	>	?			!	@	#	\$	%	&
LLHL	(3)		"	#	\$	%	&	'			()	*	+	=	>
LLHH	(4)		@	1	2	3	4	5			6	7	8	9	:	;
LHLL	(5)		*	+	=	>	?	!			@	#	\$	%	&	'
LHLH	(6)		<	=	>	?	!	@			1	2	3	4	5	6
LHHL	(7)		0	1	2	3	4	5			6	7	8	9	:	;
LHHH	(8)		"	#	\$	%	&	'			()	*	+	=	>
HLLL	(1)		0	1	2	3	4	5			6	7	8	9	:	;
HLLH	(2)		:	;	<	=	>	?			!	@	#	\$	%	&
HLHL	(3)		"	#	\$	%	&	'			()	*	+	=	>
HLHH	(4)		@	1	2	3	4	5			6	7	8	9	:	;
HHLL	(5)		*	+	=	>	?	!			@	#	\$	%	&	'
HHLH	(6)		<	=	>	?	!	@			1	2	3	4	5	6
HHHL	(7)		0	1	2	3	4	5			6	7	8	9	:	;
HHHH	(8)		"	#	\$	%	&	'			()	*	+	=	>

5. NOTES

Safety

- If the LCD panel breaks, be careful not to get the liquid crystal in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

Handling

- Avoid static electricity as this can damage the CMOS LSI.
- The LCD panel is plate glass; do not hit or crush it.
- Do not remove the panel or frame from the module.
- The polarizing plate of the display is very fragile; handle it very carefully

Mounting and Design

- Mount the module by using the specified mounting part and holes.
- To protect the module from external pressure, leave a small gap by placing transparent plates (e.g. acrylic or glass) on the display surface, frame, and polarizing plate
- Design the system so that no input signal is given unless the power-supply voltage is applied.
- Keep the module dry. Avoid condensation, otherwise the transparent electrodes may break.

Storage

- Store the module in a dark place where the temperature is $25^{\circ}\text{C}\pm 10^{\circ}\text{C}$ and the humidity below 65% RH.
- Do not store the module near organic solvents or corrosive gases.
- Do not crush, shake, or jolt the module (including accessories).

Cleaning

- Do not wipe the polarizing plate with a dry cloth, as it may scratch the surface.
- Wipe the module gently with soft cloth soaked with a petroleum benzine.
- Do not use ketonic solvents (ketone and acetone) or aromatic solvents (toluene and xylene), as they may damage the polarizing plate.

6. OPERATION PRECAUTIONS

Any changes that need to be made in this specification or any problems arising from it will be dealt with quickly by discussion between both companies.

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7. LCM Dimensions

